



INTERFACE MODULE
QS SERIES



SDM-USB-QS-S USB MODULE DATA GUIDE

DESCRIPTION

The Linx QS Series USB module allows the rapid addition of USB to virtually any device. Housed in a compact SMD package the QS module provides a complete solution for converting between USB and logic level serial sources. The module can be directly connected to virtually any serial device including microprocessors, RS232/RS485 level converters, or Linx wireless RF modules. The QS module is completely self contained and requires no external components, (except a USB jack) and includes all necessary firmware and drivers, freeing the designer from complicated programming. Power can be supplied externally or from the USB bus. Both USB 1.1 and USB 2.0 are supported at data rates to 3Mbps.

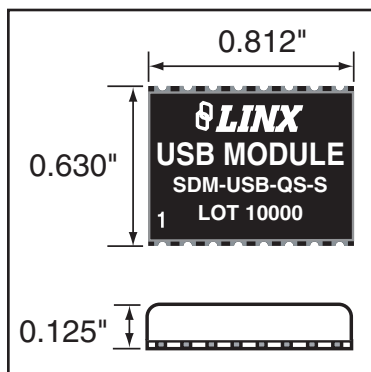


Figure 1: Package Dimensions

FEATURES

- Single Chip USB-to-Asynchronous Serial Data Conversion
- Low-Cost
- 3Mbps baud rate
- Supports Low-Speed USB
- Full Handshaking Support for RS232 and RS485
- Bus-or-Self Powered
- VID, PID, Serial Number, and Descriptors Programmed via USB
- No External Components Needed (Except a USB Jack)
- Compact Surface-mount Package
- Drivers and Firmware Included
- Supports Windows 98/2000/ME/XP
- USB 1.1 and 2.0 Compatible

APPLICATIONS INCLUDE

- Interface / Upgrade Legacy Peripherals
- Interfacing Microcontrollers To USB
- USB-to-RS232 / RS485 Converters
- Interfacing RF Modules To USB
- USB Smart Card Readers
- USB Modems
- Robotics
- USB Instrumentation
- USB Game Controllers
- USB-to-Serial Converter Cables

ORDERING INFORMATION

PART #	DESCRIPTION
SDM-USB-QS-S	USB Module
MDEV-USB-QS	Master Development Kit

ABSOLUTE MAXIMUM RATINGS

Supply voltage V_{CC}	-0.5	to	+6.0	VDC
Max Current Sourced By Data Pins			2	mA
Max Current Sunk By Data Pins			4	mA
Operating temperature	0	to	+70	°C
Storage temperature	-40	to	+90	°C
Soldering temperature	+225°C for 10 seconds			
Any input or output Pin	-0.5	to	$V_{CC} + 0.5$	VDC

NOTE Exceeding any of the limits of this section may lead to permanent damage to the device. Furthermore, extended operation at these maximum ratings may reduce the life of this device.

ELECTRICAL SPECIFICATIONS

Parameter	Designation	Min.	Typical	Max.	Units	Notes
POWER SUPPLY						
Operating Voltage	V_{CC}	4.4	5.0	5.26	VDC	—
Supply Current	I_{CC}	—	26	28	mA	—
UART SECTION						
Data Rate	—	0.0003	—	3	Mbps	—
Data Output						
Logic Low	V_{OL}	0.1	—	0.7	VDC	—
Logic High	V_{OH}	4.4	—	4.9	VDC	—
EEPROM Size		—	—	1024	Bits	—
USB SECTION						
Data Output						
Logic Low	UV_{OL}	0	—	0.3	VDC	—
Logic High	UV_{OH}	2.8	—	3.6	VDC	—
Single Ended RX Threshold	—	0.8	—	2.0	VDC	—
Differential Common Mode	—	0.8	—	2.5	VDC	—
Differential Input Sensitivity	—	0.2	—	—	VDC	—
ENVIRONMENTAL						
Operating Temperature Range	—	0	—	+70	°C	—



CAUTION

This product incorporates numerous static-sensitive components. Always wear an ESD wrist strap and observe proper ESD handling procedures when working with this device. Failure to observe this precaution may result in module damage or failure.

PIN ASSIGNMENTS

1	USBDP	RI	16
2	USBDM	DCD	15
3	GND	DSR	14
4	VCC	DATA_IN	13
5	SUSP_IND	DATA_OUT	12
6	RX_IND	RTS	11
7	TX_IND	CTS	10
8	485_TX	DTR	9

Figure 2: SDM-USB-QS-S Pinout (Top View)

PIN DESCRIPTIONS

Pin #	Name	Description
1	USBDP	USB data signal plus.
2	USBDM	USB data signal minus.
3	GND	Ground supply.
4	VCC	Positive power supply.
5	SUSP_IND	Goes low during USB Suspend Mode. This pin can be used to power down external logic when the host puts the USB bus into suspend mode.
6	RX_IND	This line will pulse low when receiving data from the USB bus. This allows for the connection of a LED indicator.
7	TX_IND	This line will pulse low when transmitting data on the USB bus. This allows for the connection of a LED indicator.
8	485_TX	Transmit enable line for RS485 applications.
9	DTR	Data Terminal Ready control / handshake output
10	CTS	Clear To Send control / handshake input
11	RTS	Request To Send control / handshake output
12	DATA_OUT	Transmit asynchronous data output
13	DATA_IN	Receive asynchronous data input
14	DSR	Data Set Ready control / handshake input
15	DCD	Data Carrier Detect control / input
16	RI	Ring Indicator control input

PAD LAYOUT

The following pad layout diagram is designed to facilitate both hand and automated assembly.

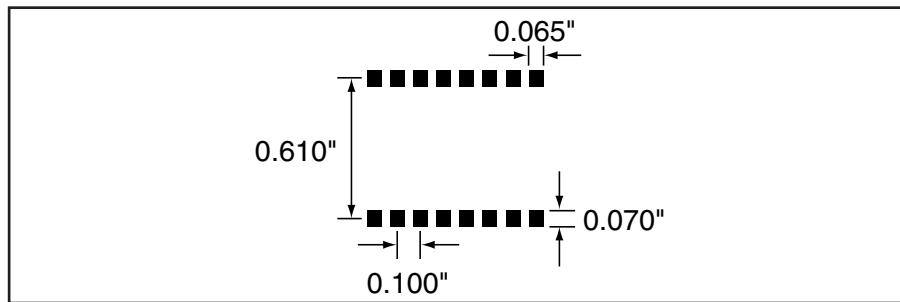


Figure 3: Recommended PCB Layout

PRODUCTION GUIDELINES

The modules are housed in a hybrid SMD package that supports hand or automated assembly techniques. Since the modules contain discrete components internally, the assembly procedures are critical to ensuring the reliable function of the modules. The following procedures should be reviewed with and practiced by all assembly personnel.

HAND ASSEMBLY

The module's primary mounting surface is sixteen pads located on the bottom of the module. Since these pads are inaccessible during mounting, castellations that run up the side of the module have been provided to facilitate solder wicking to the module's underside. This allows for very quick hand soldering for prototyping and small volume production.

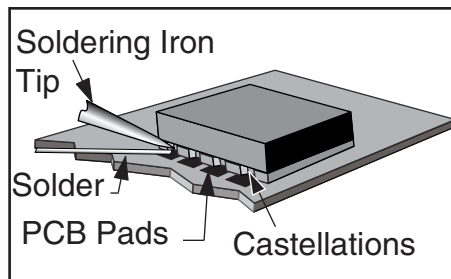


Figure 4: Soldering Technique

If the recommended pad guidelines have been followed, the pads will protrude slightly past the edge of the module. Use a fine soldering tip to heat the board pad and the castellation, then introduce solder to the pad at the module's edge. The solder will wick underneath the module providing reliable attachment. Tack one module corner first and then work around the device taking care not to exceed the times listed below.

Absolute Maximum Solder Times

Hand-Solder Temp. TX +225°C for 10 Seconds

Hand-Solder Temp. RX +225°C for 10 Seconds

Recommended Solder Melting Point +180°C

Reflow Oven: +220°C Max. (See adjoining diagram)

AUTOMATED ASSEMBLY

For high-volume assembly most users will want to auto-place the modules. The modules have been designed to maintain compatibility with reflow processing techniques, however, due to their hybrid nature certain aspects of the assembly process are far more critical than for other component types.

Following are brief discussions of the three primary areas where caution must be observed.

Reflow Temperature Profile

The single most critical stage in the automated assembly process is the reflow process. The reflow profile below should not be exceeded since excessive temperatures or transport times during reflow will irreparably damage the modules. Assembly personnel will need to pay careful attention to the oven's profile to ensure that it meets the requirements necessary to successfully reflow all components while still remaining within the limits mandated by the modules themselves. The figure below shows the recommended reflow oven profile for the modules.

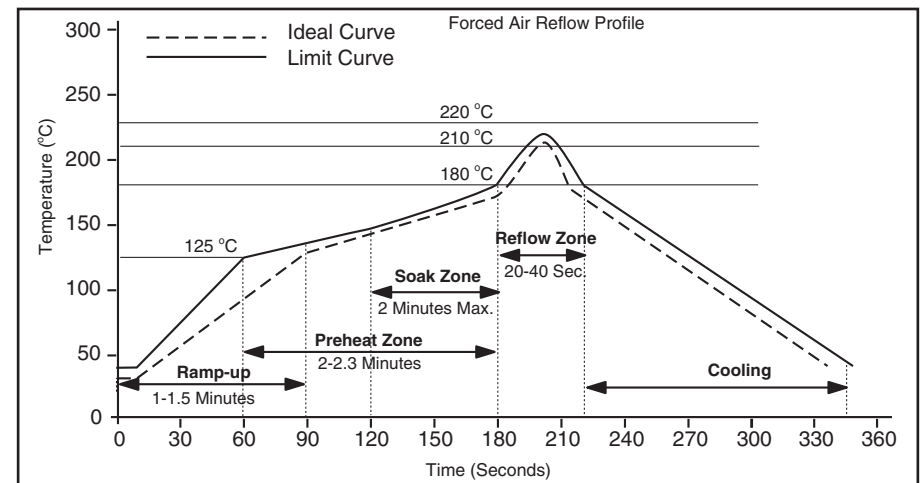


Figure 5: Maximum Reflow Profile

Shock During Reflow Transport

Since some internal module components may reflow along with the components placed on the board being assembled, it is imperative that the modules not be subjected to shock or vibration during the time solder is liquid. Should a shock be applied, some internal components could be lifted from their pads, causing the module to not function properly.

Washability

The modules are wash resistant, but are not hermetically sealed. Linx recommends wash-free manufacturing, however, the modules can be subjected to a wash cycle provided that a drying time is allowed prior to applying electrical power to the modules. The drying time should be sufficient to allow any moisture that may have migrated into the module to evaporate, thus eliminating the potential for shorting damage during power-up or testing. If the wash contains contaminants, the performance may be adversely affected, even after drying.

MODULE DESCRIPTION

The Linx SDM-USB-QS-S module will convert USB signals from a host, such as a PC or hub, into TTL logic level signals. This enables the module to be connected directly to microcontrollers (or Linx RF modules for wireless applications) or to RS232 or RS485 level converters for communication with legacy devices. The module handles all of the complicated enumeration and bus communication processes thus freeing the designer to focus on handling the data. All necessary firmware is included in the module and the device descriptors can easily be changed to customize the device.

The host application software can access the USB device by simple custom functions or by standard Windows Win32 API calls. In addition, Virtual Com Port drivers are available that make the USB module appear to the PC as an additional COM port without the need for additional system resources, such as an IRQ or address. This allows the designer to program the application software to use standard serial or parallel ports and then to simply select the port that represents the USB module. The drivers will then automatically direct the data to the USB bus and the device.

INSTALLING THE DRIVERS

The drivers for the USB module are included with the module's development system or may be downloaded from the Linx web site (www.linxtechnologies.com). These drivers should be downloaded onto the hard drive of a PC or onto a disk. When the module is attached to the PC for the first time Windows will automatically detect the device and search for the best driver. The user will be prompted to provide a location for Windows to find the drivers, so the user will then browse to the folder or the disk, click Next and Windows will do the rest. Windows XP may return an error window shown in the figure below.



Figure 6: Windows XP Driver Error Window

This window is simply a warning that the driver has not gone through Microsoft's certification process and could potentially pose a problem for the system. The drivers provided for the QS module have been independently tested and should not pose any problems unless modified by the user. Click the Continue Anyway button to finish the installation process.

THEORY OF OPERATION

Figure 7 below shows a block diagram of the QS module.

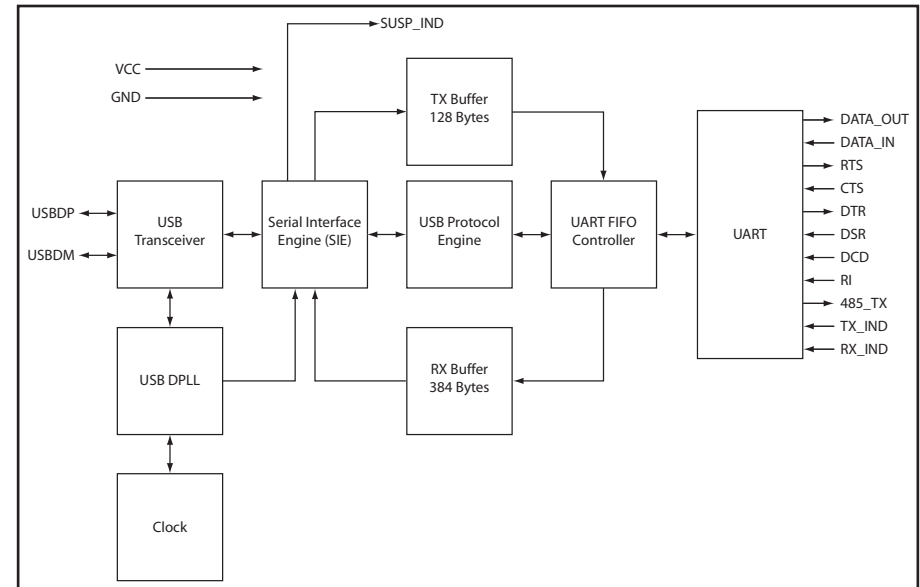


Figure 7: SDM-USB-QS-S Block Diagram

The USB transceiver block provides the physical interface for the USB signalling. The USB DPLL locks onto the NRZ data and provides separate recovered clock and data signals to the Serial Interface Engine (SIE).

The SIE performs the parallel to serial and serial to parallel conversion, bit-stuffing/un-stuffing, and CRC calculations on the USB data.

The USB Protocol Engine manages the data from the USB control endpoint, the USB protocol requests from the USB host controller, and the commands for controlling the functional parameters of the UART.

Data from the USB data out endpoint is stored in the TX buffer and removed from the buffer to the UART transmit register under control of the UART FIFO controller.

Data from the UART receive register is stored in the RX buffer prior to being removed by the SIE on a USB request for data from the device data in endpoint.

The UART FIFO controller handles the transfer of data between the RX and TX buffers and the UART transmit and receive registers.

The UART performs asynchronous 7/8 bit parallel to serial and serial to parallel conversion of the data on the RS232 (RS422 and RS485) interface. Control signals supported by the UART include RTS, CTS, DSR, DTR, DCD and RI. The UART provides a transmitter enable control signal (485_TX) to assist with interfacing to RS485 transceivers. The UART supports RTS/CTS, DSR/DTR and X-On/X-Off handshaking options. Handshaking, where required, is handled in hardware to ensure fast response times. The UART also supports the RS232 BREAK setting and detection conditions.

TYPICAL APPLICATIONS (CONT)

Figure 11 below shows the QS module connected to a microprocessor. This is the design used in the QS Master Development Kit and the documentation for the kit describes the connections and software.

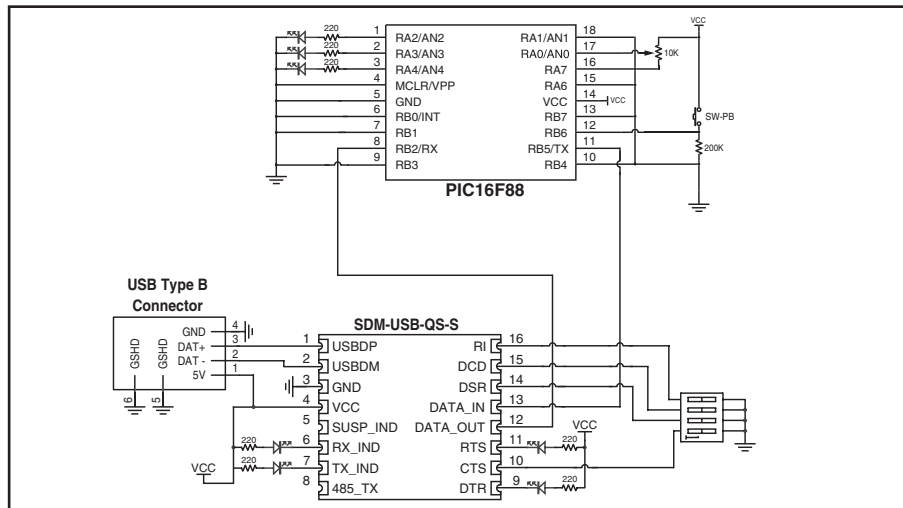


Figure 11: Interface With A Microprocessor

SOFTWARE CONSIDERATIONS

The host application can access the QS module in two ways. First is through Virtual COM Port drivers. These drivers make the QS appear as an extra COM port on the host PC. This allows the application to use standard writes and reads to a serial port and the drivers will redirect data to the USB device.

Second are a series of custom functions supported by the direct driver .dlls. These functions are also described in the Programmer's Guide where examples are given in both Visual Basic and C. The Programmer's guide can be downloaded from the Linx web site (www.linxtechnologies.com).

In addition to the Programmer's Guide, the QS Master Development Kit (MDEV-USB-QS) includes example software and sample system source code. This source code provides the driver function declarations, examples of how to use the functions in a program, and other code that may be of use.

ON-LINE RESOURCES



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